

FIG. 1

FIG. 2 is a block diagram of an error correction system.

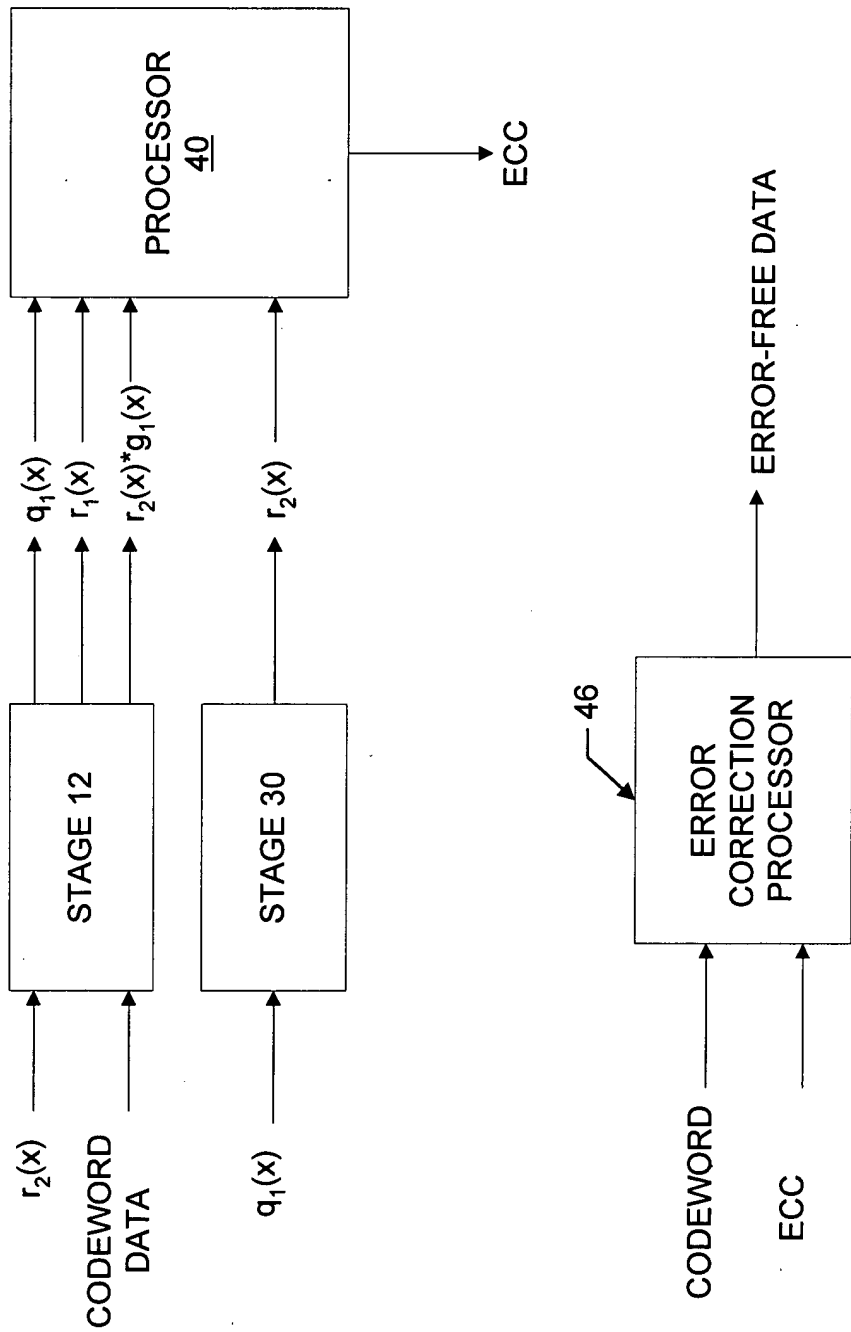


FIG. 2

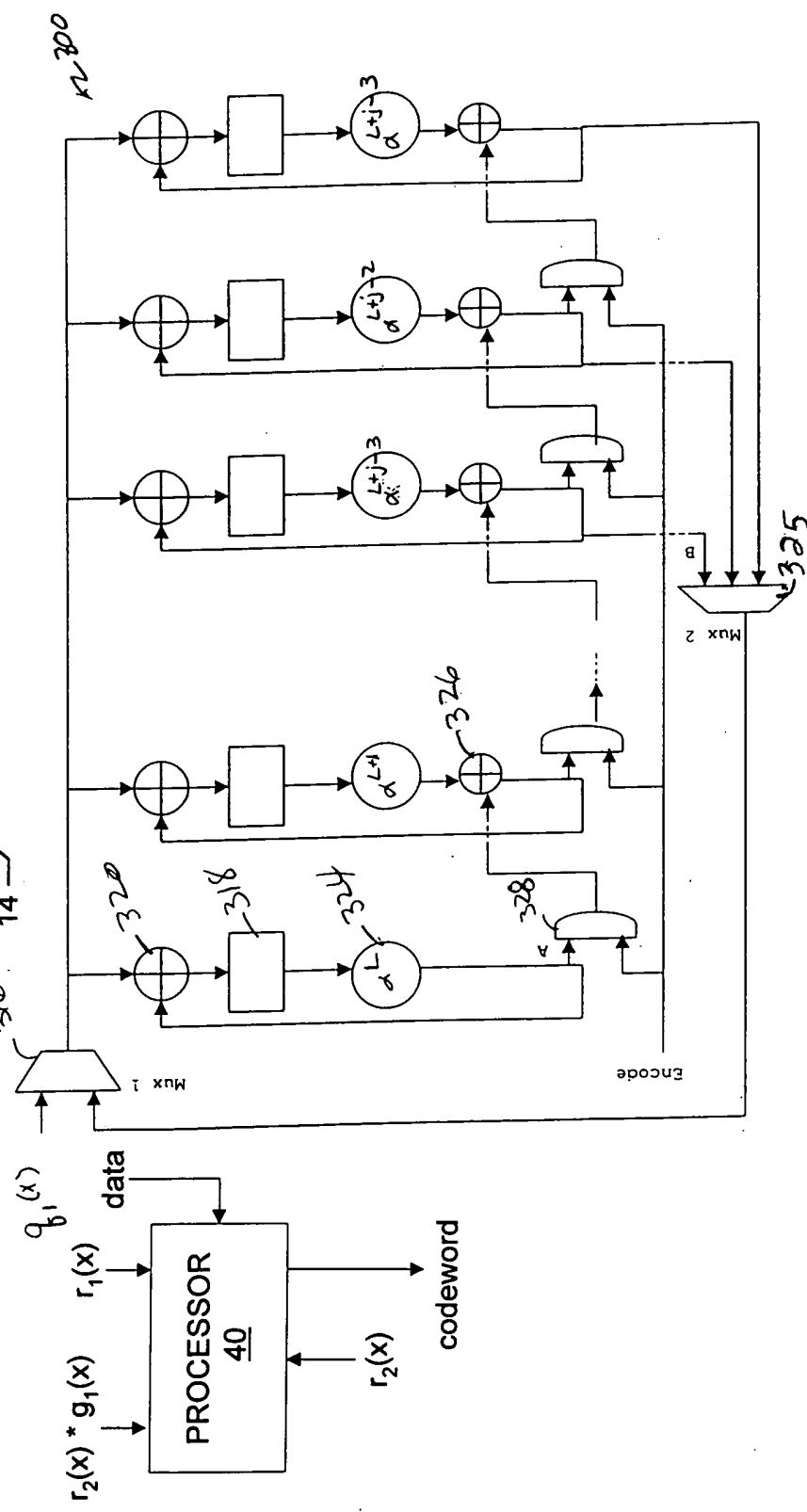
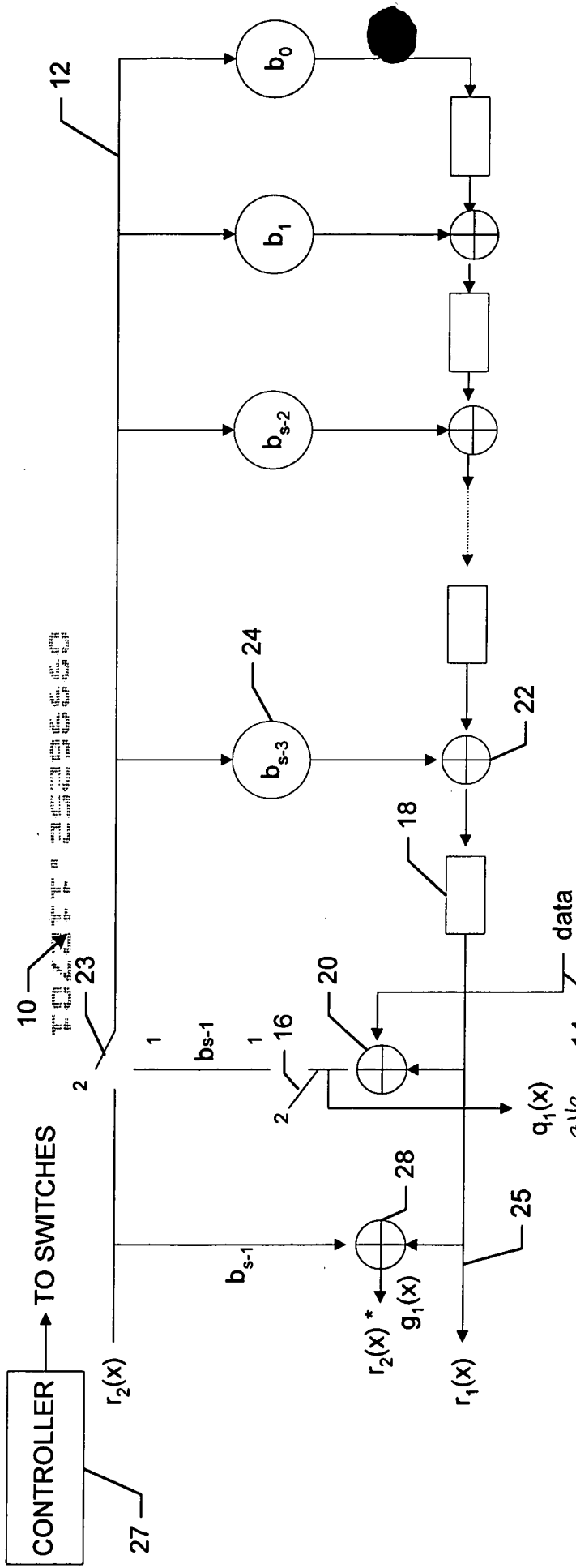


FIG. 3

FIG. 4 is a block diagram of a system for error correction.

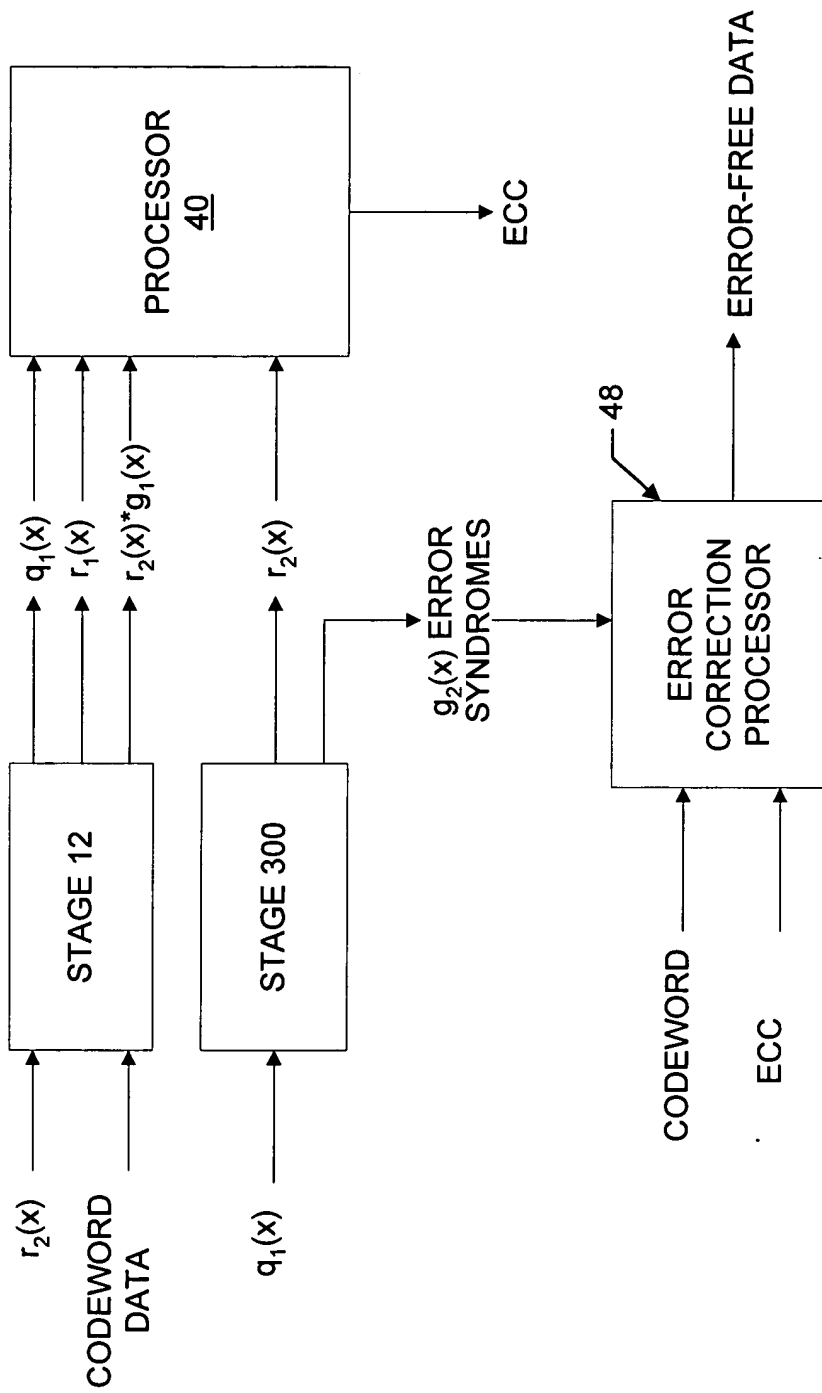


FIG. 4